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EXPEDITED PROCEDURE  
EXAMINING GROUP 2815

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of : Yoshinori UEDA

Serial No. : 09/431,593 ✓

Date Filed : November 1, 1999

For : SEMICONDUCTOR DEVICE HAVING AN  
INTEGRAL RESISTANCE ELEMENT

Group Art Unit: 2815

Examiner: M. E. Warren

TECHNOLOGY CENTER 2800  
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1185 Avenue of the Americas  
New York, N.Y. 10036

Assistant Commissioner for Patents  
BOX AF  
Washington, D.C. 20231

**AMENDMENT AFTER FINAL REJECTION**

Sir:

In response to the final Office Action dated April 1, 2002, please amend the above-identified application as follows:

**In the Claims:**

Please amend claims 1 as follows:

1. (Twice Amended) A semiconductor device, comprising:

I hereby certify that this paper is being deposited this date with the U.S. Postal Service as first class mail addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Richard F. Jaworski  
Reg. No. 33,515

June 24, 2002  
Date

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